

## 1Mbit/s High Speed Transistor Photo Coupler

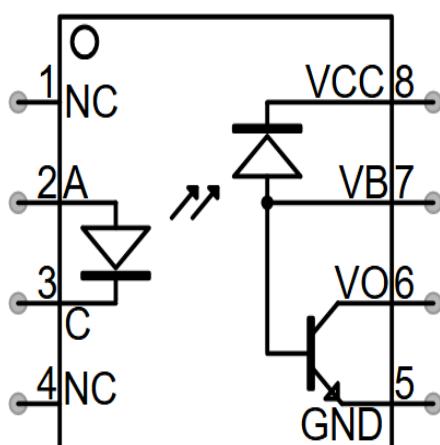
### Description

The 6N135/ 6N136 consists of a high efficient AlGaAs Light Emitting Diode and a high speed optical detector.

This design provides excellent AC and DC isolation between the input and output sides of the Optocoupler. Connection for the bias of the photodiode improves the speed that of a conventional phototransistor coupler by reducing the base-collector capacitances.

The internal shield ensures high common mode transient immunity. A guaranteed common mode transient immunity is up to 1KV

### Functional Diagram



6N135/6N136

### Features

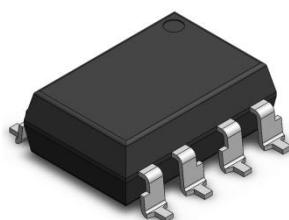
- High speed – 1MBd typical
- High isolation voltage between input and output ( $V_{iso}=5000$  Vrms )
- Operating temperature range - 55 °C to 100 °C
- Guaranteed performance from 0°C to 70°C

### Applications

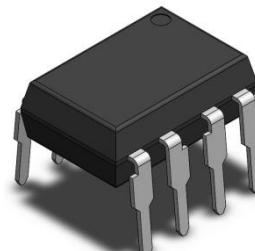
- Line receivers
- Telecommunication equipment
- Out interface to CMOS-LSTTL-TTL
- Wide bandwidth analog coupling
- Pulse transformer replacement

### Truth Table (Positive Logic)

LED	VO
ON	LOW
OFF	HIGH



6N135 6N136/SMD-8



6N135 6N136/DIP-8

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNIT	Note
Forward Current	I <sub>F</sub>	25	mA	
Peak Forward Current	I <sub>FP</sub>	50	mA	1
Peak Transient Current	I <sub>F(trans)</sub>	1	A	2
Reverse Voltage	V <sub>R</sub>	5	V	
Input Power Dissipation	P <sub>I</sub>	100	mW	
Supply Voltage	V <sub>CC</sub>	-0.5~30	V	
Output Voltage	V <sub>O</sub>	-0.5~20	V	
Output Current	I <sub>O</sub>	50	mA	
Emitter-Base Reverse Voltage	V <sub>EBR</sub>	5	V	
Base Current	I <sub>B</sub>	5	mA	
Output Power Dissipation	P <sub>O</sub>	100	mW	
Total Power Dissipation	P <sub>TOT</sub>	200	mW	
Isolation Voltage	V <sub>ISO</sub>	5000	Vrms	3
Operating Temperature	T <sub>OPR</sub>	-55~100	°C	
Storage Temperature	T <sub>STG</sub>	-55~150	°C	
Soldering Temperature	T <sub>SOL</sub>	260	°C	4

Note 1. 50% duty, 1ms PW

Note 2. ≤1μs PW, 300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

Note 4. For 10 seconds

## ELECTRICAL OPTICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
INPUT(at Ta=0 to 70°C , unless specified otherwise)							
Forward Voltage	V <sub>F</sub>	-	1.45	1.8	V	I <sub>F</sub> =16mA	
Reverse Current	I <sub>R</sub>	-	-	10	μA	V <sub>R</sub> =5V	
Input Capacitance	C <sub>IN</sub>	-	60	-	pF	V=0, f=1MHz	
OUTPUT(at Ta=0 to 70°C , unless specified otherwise)							
High Level Supply Current	I <sub>CCH</sub>	-	0.01	1	μA	I <sub>F</sub> =0mA, V <sub>O</sub> =Open, V <sub>CC</sub> =15V, Ta=25°C	
		-	-	2	μA	I <sub>F</sub> =0mA, V <sub>O</sub> =Open, V <sub>CC</sub> =15V	
Low Level Supply Current	I <sub>CCL</sub>	-	200	-	μA	I <sub>F</sub> =16mA, V <sub>O</sub> =Open, V <sub>CC</sub> =15V	
Logic High Output Current	I <sub>OH</sub>	-	0.001	0.5	μA	I <sub>F</sub> =0mA, V <sub>O</sub> =V <sub>CC</sub> =5.5V, Ta=25°C	
		-	0.01	1	μA	I <sub>F</sub> =0mA, V <sub>O</sub> =V <sub>CC</sub> =15V, Ta=25°C	
		-	-	50	μA	I <sub>F</sub> =0mA, V <sub>O</sub> =V <sub>CC</sub> =15V	

ELECTRICAL OPTICAL CHARACTERISTICS (TA = 25°C unless otherwise noted)							
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
TRANSFER CHARACTERISTICS(at Ta=0 to 70°C , unless specified otherwise)							
Current Transfer Ratio	6N135	CTR	7	-	50	$\mu\text{A}$	$I_F = 16\text{mA}, V_O = 0.4\text{V}, V_{CC}=4.5\text{V}, Ta=25^\circ\text{C}$
	6N136		19	-	50		
	6N135		5	-	-		$I_F = 16\text{mA}, V_O = 0.5\text{V}, V_{CC}=4.5\text{V}$
	6N136		15	-	-		
Logic Low Output Voltage	6N135	V <sub>OL</sub>	-	0.18	0.4	$\mu\text{A}$	$I_F = 16\text{mA}, I_O = 1.1\text{mA}, V_{CC}=4.5\text{V}, Ta=25^\circ\text{C}$
	6N136		-	0.25	0.4		
	6N135		-	-	-		$I_F = 16\text{mA}, I_O = 0.8\text{mA}, V_{CC}=4.5\text{V}$
	6N136		-	-	-		
Isolation Resistance	R <sub>iso</sub>	10 <sup>12</sup>	10 <sup>14</sup>	-	$\Omega$	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C <sub>IO</sub>	-	0.3	1	pF	V=0, f=1MHz	

ELECTRICAL OPTICAL CHARACTERISTICS								
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE	
SWITCHING CHARACTERISTICS(at Ta=0 to 70°C, $I_F=16\text{mA}$ , $V_{CC}=5\text{V}$ , unless specified otherwise)								
Propagation Delay Time to Logic Low	6N135	TPHL	-	0.35	1.5	$\mu\text{s}$	$R_L=4.1\text{k}\Omega, T_A=25^\circ\text{C}$	
			-	-	2		$R_L=4.1\text{k}\Omega$	
	6N136		-	0.35	0.8		$R_L=1.9\text{k}\Omega, T_A=25^\circ\text{C}$	
			-	-	1.0		$R_L=1.9\text{k}\Omega$	
Propagation Delay Time to Logic High	6N135	TPLH	-	0.5	1.5	$\mu\text{s}$	$R_L=4.1\text{k}\Omega, T_A=25^\circ\text{C}$	
			-	-	2		$R_L=4.1\text{k}\Omega$	
	6N136		-	0.3	0.8		$R_L=1.9\text{k}\Omega, T_A=25^\circ\text{C}$	
			-	-	1.0		$R_L=1.9\text{k}\Omega$	
Common Mode Transient Immunity at Logic High	6N135	CM <sub>H</sub>	1000	-	-	V/ $\mu\text{s}$	$I_F = 0\text{mA}, V_{CM}=10\text{Vpp}, R_L=4.1\text{k}\Omega, T_A = 25^\circ\text{C}$	
	6N136		1000	-	-		$I_F = 0\text{mA}, V_{CM}=10\text{Vpp}, R_L=1.9\text{k}\Omega, T_A = 25^\circ\text{C}$	
Common Mode Transient Immunity at Logic Low	6N135	CM <sub>L</sub>	1000	-	-	V/ $\mu\text{s}$	$I_F = 16\text{mA}, V_{CM}=10\text{Vpp}, R_L=4.1\text{k}\Omega, T_A = 25^\circ\text{C}$	
	6N136		1000	-	-		$I_F = 16\text{mA}, V_{CM}=10\text{Vpp}, R_L=1.9\text{k}\Omega, T_A = 25^\circ\text{C}$	

Fig.13

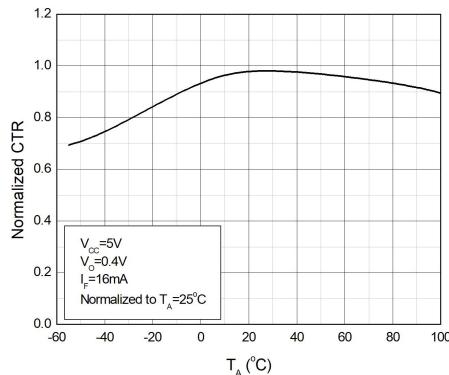
Fig.13

Fig.14

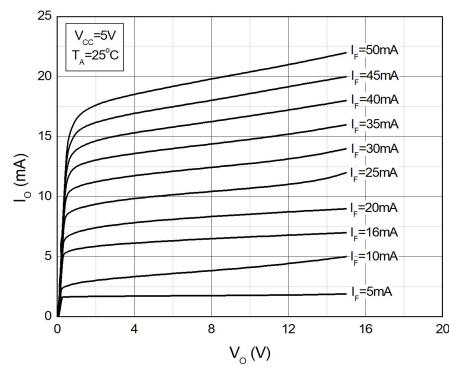
Fig.14

## CHARACTERISTIC CURVES

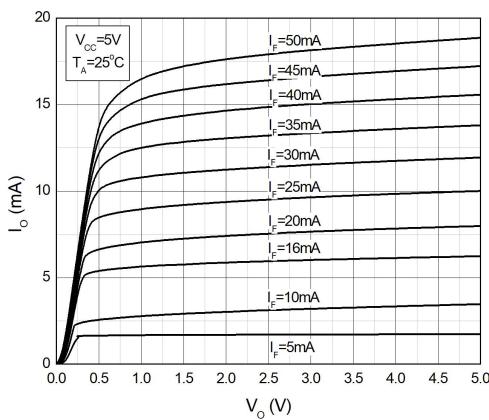
**Fig.1 Input Threshold Current  
vs. Ambient Temperature**



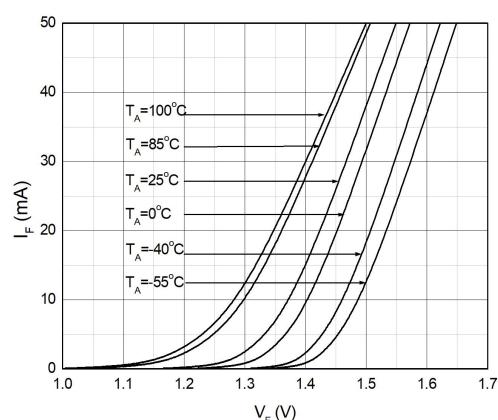
**Fig.2 Low Level Output Current  
vs. Ambient Temperature**



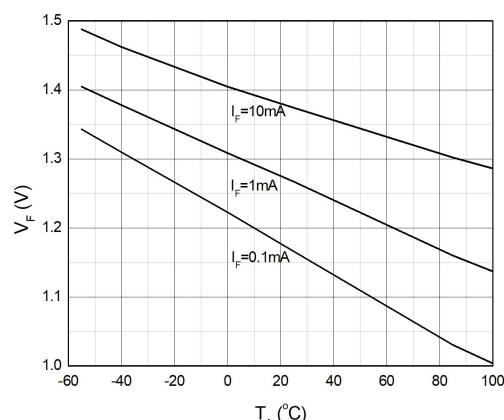
**Fig.3 Low Level Output Current  
vs. Ambient Temperature**



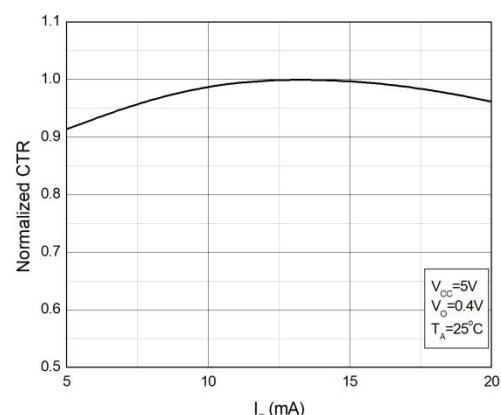
**Fig.4 Forward Current  
vs. Forward Voltage**



**Fig.5 Forward Voltage  
vs. Ambient Temperature**

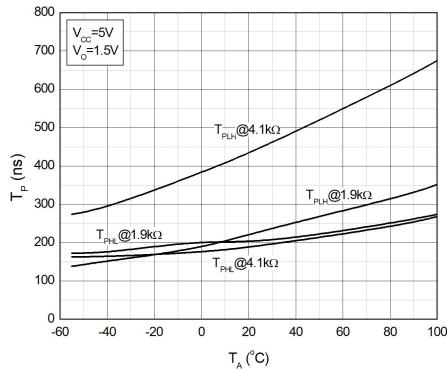


**Fig.6 Input Threshold Current  
vs. Ambient Temperature**

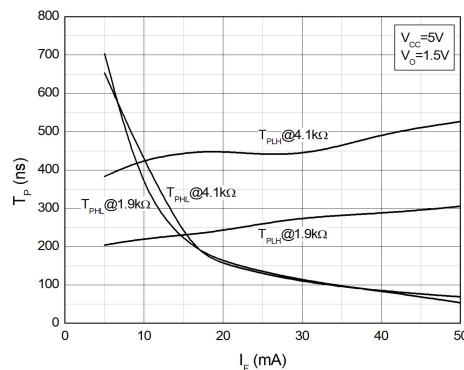


## CHARACTERISTIC CURVES

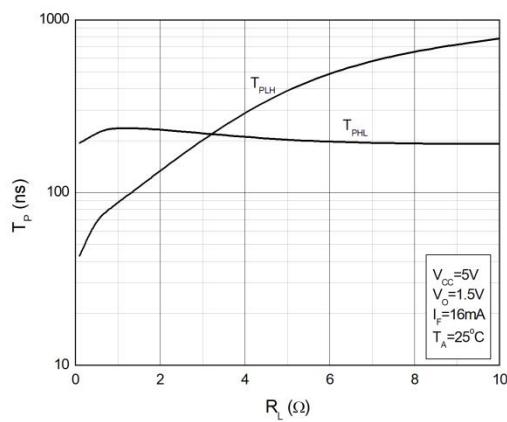
**Fig.7 Output Voltage  
vs. Forward Current**



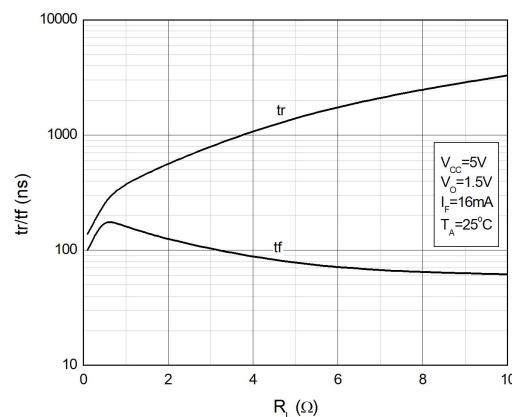
**Fig.8 Output Voltage  
vs. Forward Current**



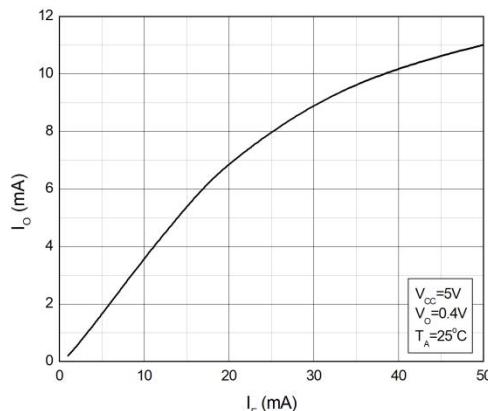
**Fig.9 High Level Output Current  
vs. Ambient Temperature**



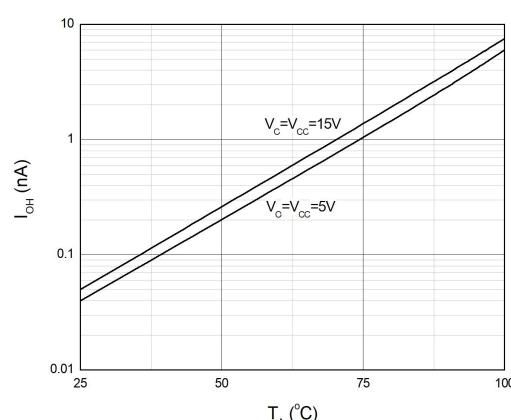
**Fig.10 High Level Output Current  
vs. Ambient Temperature**



**Fig.11 Low Level Output Voltage  
vs. Ambient Temperature**



**Fig.12 Low Level Output Voltage  
vs. Ambient Temperature**



## TEST CIRCUITS

Fig.13 Test Circuits for TPHL, TPLH, tr, tf

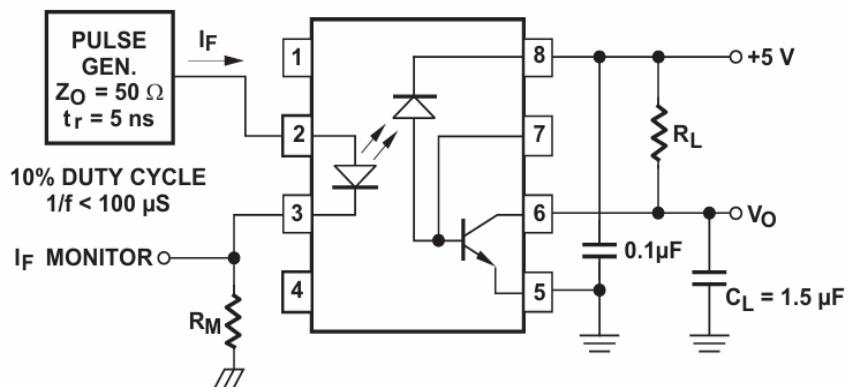
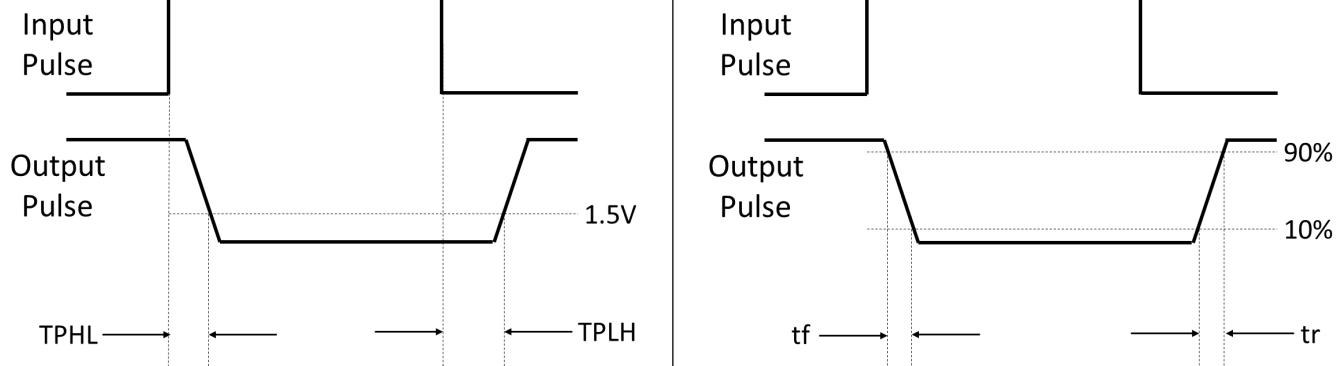
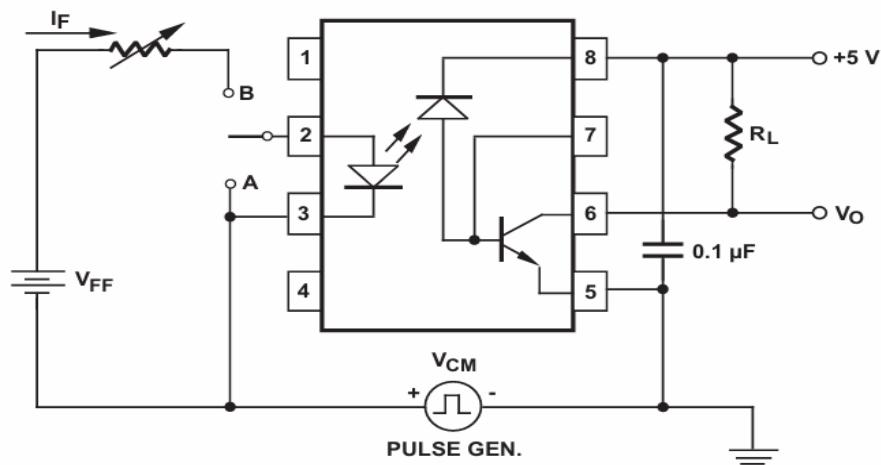


Fig.14 Waveforms of TPHL, TPLH, tr, tf

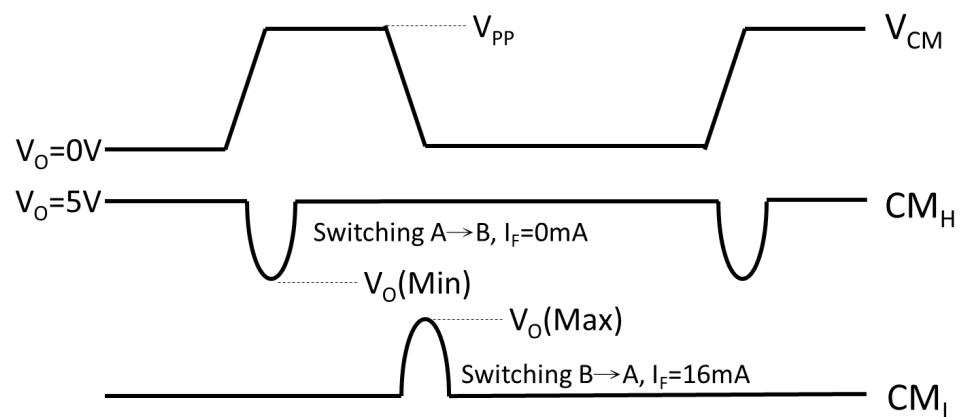


## TEST CIRCUITS

**Fig.15 Test Circuits for Common Mode Transient Immunity**

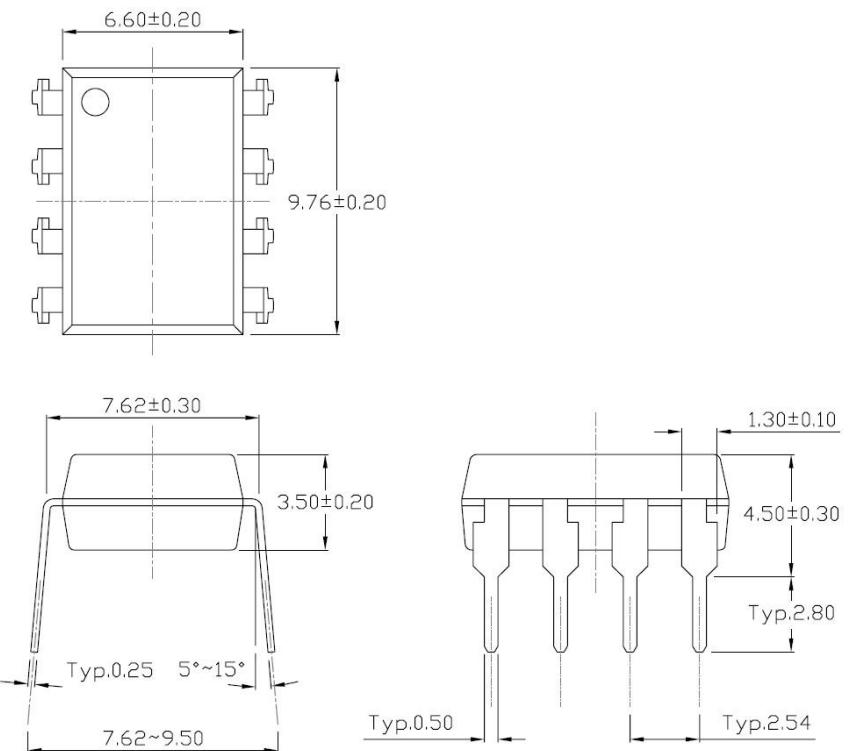


**Fig.16 Waveforms of Common Mode Transient Immunity**

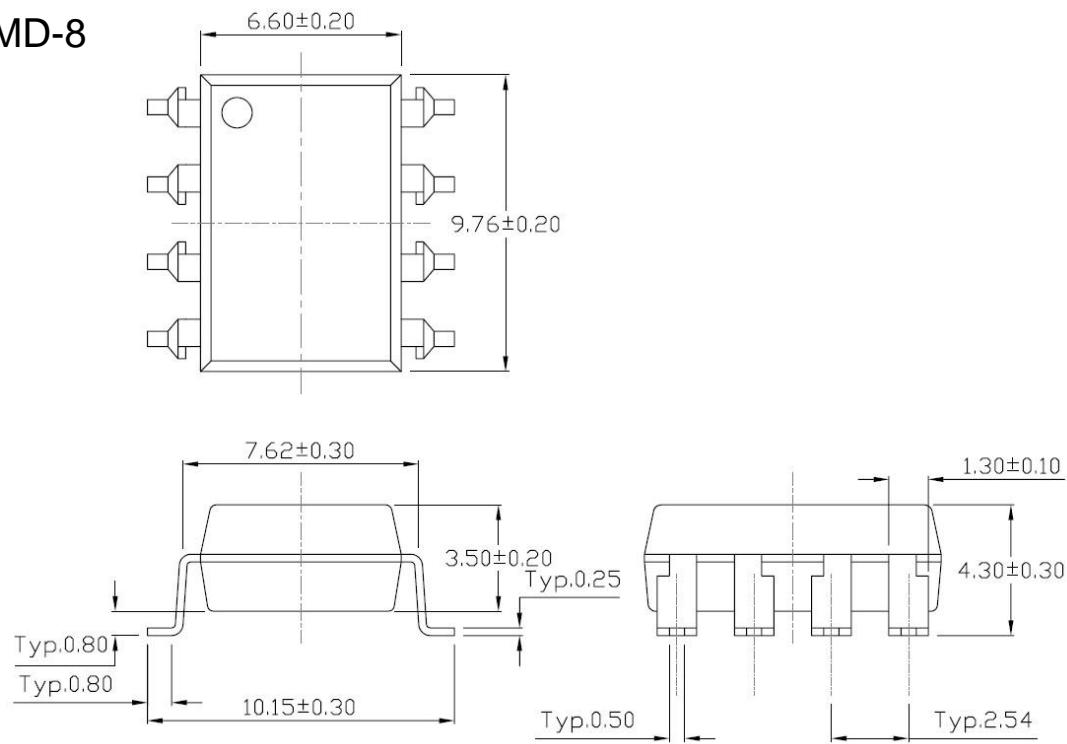


## PACKAGE DIMENSIONS Dimensions in mm unless otherwise stated

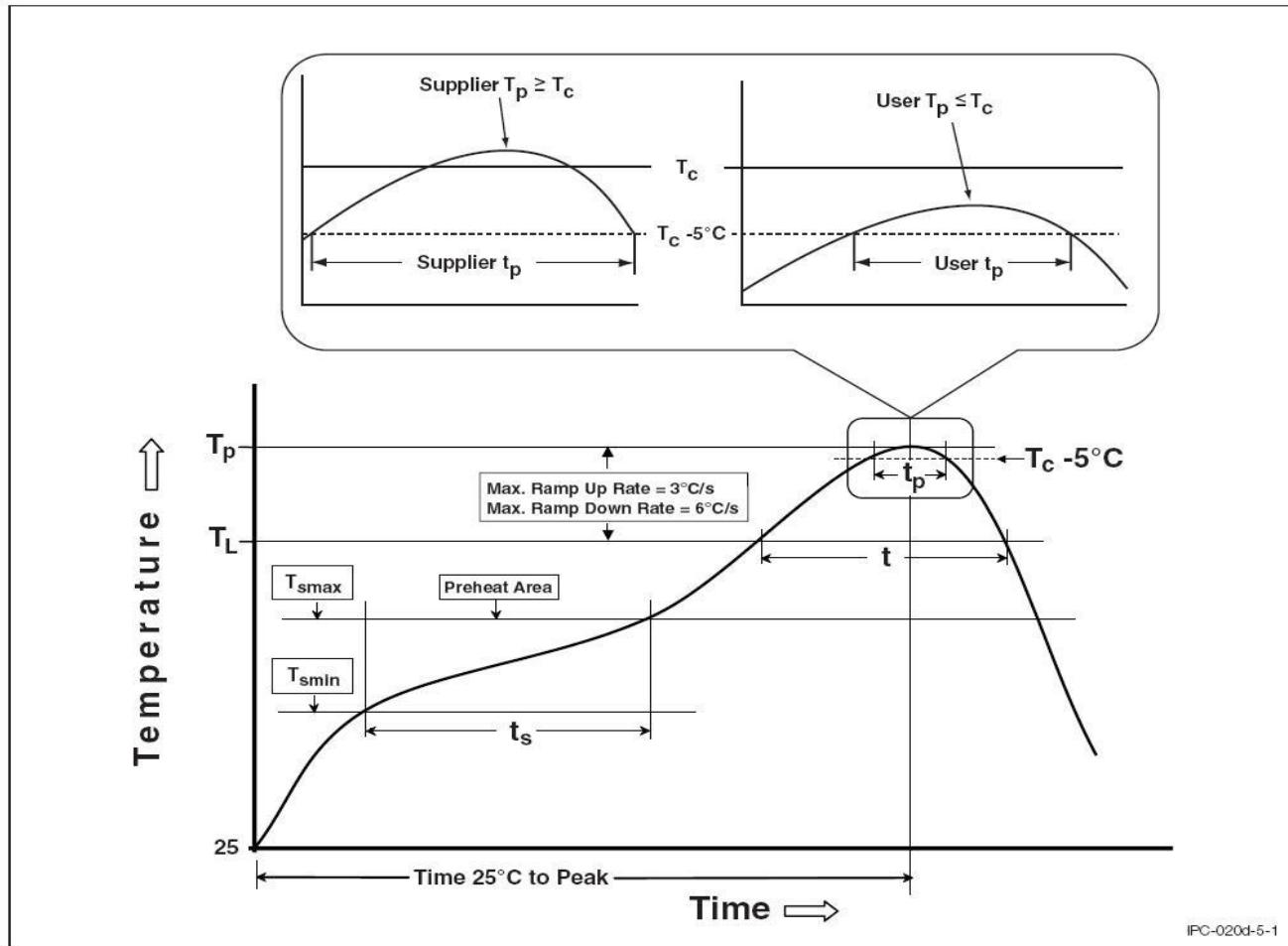
DIP-8



SMD-8



## REFLOW INFORMATION



REFLOW PROFILE

IPC-020d-5-1

Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. ( $T_{smin}$ )	100	150°C
Temperature Max. ( $T_{smax}$ )	150	200°C
Time ( $t_s$ ) from ( $T_{smin}$ to $T_{smax}$ )	60-120 seconds	60-120 seconds
Ramp-up Rate ( $t_L$ to $t_P$ )	3°C/second max.	3°C/second max.
Liquidous Temperature ( $T_L$ )	183°C	217°C
Time ( $t_L$ ) Maintained Above ( $T_L$ )	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time ( $t_P$ ) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate ( $T_P$ to $T_L$ )	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.